



## Surefire Report Summary

[\[Summary\]](#) [\[Package List\]](#) [\[Test Cases\]](#)

Tests	Errors	Failures	Skipped	Success Rate	Time
132	0	0	0	100%	72.473

Note: failures are anticipated and checked for with assertions while errors are unanticipated.












## Package List

[\[Summary\]](#) [\[Package List\]](#) [\[Test Cases\]](#)

Package	Tests	Errors	Failures	Skipped	Success Rate	Time
<a href="#">client</a>	132	0	0	0	100%	72.473
















Note: package statistics are not computed recursively, they only sum up all of its testsuites numbers.

### [client](#)

	Class	Tests	Errors	Failures	Skipped	Success Rate	Time
	<a href="#">ASM_10001_TestCase</a>	1	0	0	0	100%	0.141
	<a href="#">ASM_10002_TestCase</a>	1	0	0	0	100%	0.531
	<a href="#">ASM_10003_TestCase</a>	1	0	0	0	100%	0.125
	<a href="#">ASM_12002_TestCase</a>	1	0	0	0	100%	0.593
	<a href="#">ASM_12003_TestCase</a>	1	0	0	0	100%	0.578
	<a href="#">ASM_12005_TestCase</a>	1	0	0	0	100%	0.562
	<a href="#">ASM_12006_TestCase</a>	1	0	0	0	100%	0.625
	<a href="#">ASM_12007_TestCase</a>	1	0	0	0	100%	0.5
	<a href="#">ASM_12008_TestCase</a>	1	0	0	0	100%	0.531
	<a href="#">ASM_12009_TestCase</a>	1	0	0	0	100%	0.547
	<a href="#">ASM_12010_TestCase</a>	1	0	0	0	100%	0.578

🟢	ASM_12011_TestCase	1	0	0	0	100%	0.656
🟢	ASM_12012_TestCase	1	0	0	0	100%	0.313
🟢	ASM_12013_TestCase	1	0	0	0	100%	0.547
🟢	ASM_13001_TestCase	1	0	0	0	100%	0.484
🟢	ASM_13002_TestCase	1	0	0	0	100%	0.141
🟢	ASM_13003_TestCase	1	0	0	0	100%	0.125
🟢	ASM_13004_TestCase	1	0	0	0	100%	0.5
🟢	ASM_13005_TestCase	1	0	0	0	100%	0.125
🟢	ASM_13006_TestCase	1	0	0	0	100%	0.171
🟢	ASM_13007_TestCase	1	0	0	0	100%	0.11
🟢	ASM_13008_TestCase	1	0	0	0	100%	0.125
🟢	ASM_4002_TestCase	1	0	0	0	100%	0.562
🟢	ASM_4003_TestCase	1	0	0	0	100%	0.516
🟢	ASM_4004_TestCase	1	0	0	0	100%	0.516
🟢	ASM_4005_TestCase	1	0	0	0	100%	0.766
🟢	ASM_4006_TestCase	1	0	0	0	100%	0.641
🟢	ASM_4007_TestCase	1	0	0	0	100%	0.141
🟢	ASM_4008_TestCase	1	0	0	0	100%	0.719
🟢	ASM_5002_TestCase	1	0	0	0	100%	2.125
🟢	ASM_5003_TestCase	1	0	0	0	100%	0.563
🟢	ASM_5004_TestCase	1	0	0	0	100%	2.969
🟢	ASM_5005_TestCase	1	0	0	0	100%	0.547
🟢	ASM_5006_TestCase	1	0	0	0	100%	0.516
🟢	ASM_5007_TestCase	1	0	0	0	100%	0.516
🟢	ASM_5008_TestCase	1	0	0	0	100%	0.5
🟢	ASM_5009_TestCase	1	0	0	0	100%	0.547
🟢	ASM_5010_TestCase	1	0	0	0	100%	0.515
🟢	ASM_5011_TestCase	1	0	0	0	100%	0.594
🟢	ASM_5012_TestCase	1	0	0	0	100%	0.578
🟢	ASM_5013_TestCase	1	0	0	0	100%	0.547
🟢	ASM_5014_TestCase	1	0	0	0	100%	0.515
🟢	ASM_5015_TestCase	1	0	0	0	100%	0.61
🟢	ASM_5016_TestCase	1	0	0	0	100%	0.563
🟢	ASM_5017_TestCase	1	0	0	0	100%	0.563
🟢	ASM_5018_TestCase	1	0	0	0	100%	0.562
🟢	ASM_5019_TestCase	1	0	0	0	100%	0.609
🟢	ASM_5020_TestCase	1	0	0	0	100%	0.688
🟢	ASM_5021_TestCase	1	0	0	0	100%	0.61
🟢	ASM_5022_TestCase	1	0	0	0	100%	0.563
🟢	ASM_5023_TestCase	1	0	0	0	100%	0.532
🟢	ASM_5024_TestCase	1	0	0	0	100%	0.532
🟢	ASM_5025_TestCase	1	0	0	0	100%	0.547
🟢	ASM_5026_TestCase	1	0	0	0	100%	0.563
🟢	ASM_5027_TestCase	1	0	0	0	100%	0.515
🟢	ASM_5028_TestCase	1	0	0	0	100%	0.547
🟢	ASM_5029_TestCase	1	0	0	0	100%	0.765
🟢	ASM_5030_TestCase	1	0	0	0	100%	0.125
🟢	ASM_5031_TestCase	1	0	0	0	100%	0.578
🟢	ASM_5032_TestCase	1	0	0	0	100%	0.578
🟢	ASM_5033_TestCase	1	0	0	0	100%	0.562
🟢	ASM_5034_TestCase	1	0	0	0	100%	0.609
🟢	ASM_5035_TestCase	1	0	0	0	100%	0.125
🟢	ASM_5036_TestCase	1	0	0	0	100%	0.515

🚩	ASM_5037_TestCase	1	0	0	0	100%	0.625
🚩	ASM_5038_TestCase	1	0	0	0	100%	0.594
🚩	ASM_5039_TestCase	1	0	0	0	100%	0.531
🚩	ASM_5040_TestCase	1	0	0	0	100%	0.547
🚩	ASM_5041_TestCase	1	0	0	0	100%	0.562
🚩	ASM_5042_TestCase	1	0	0	0	100%	0.546
🚩	ASM_5043_TestCase	1	0	0	0	100%	0.641
🚩	ASM_5044_TestCase	1	0	0	0	100%	0.594
🚩	ASM_6001_TestCase	1	0	0	0	100%	0.125
🚩	ASM_6002_TestCase	1	0	0	0	100%	0.547
🚩	ASM_6003_TestCase	1	0	0	0	100%	0.516
🚩	ASM_6004_TestCase	1	0	0	0	100%	0.563
🚩	ASM_6005_TestCase	1	0	0	0	100%	0.531
🚩	ASM_6006_TestCase	1	0	0	0	100%	0.532
🚩	ASM_6007_TestCase	1	0	0	0	100%	0.547
🚩	ASM_6008_TestCase	1	0	0	0	100%	0.547
🚩	ASM_6009_TestCase	1	0	0	0	100%	0.563
🚩	ASM_6010_TestCase	1	0	0	0	100%	0.547
🚩	ASM_6011_TestCase	1	0	0	0	100%	0.562
🚩	ASM_6012_TestCase	1	0	0	0	100%	0.546
🚩	ASM_6013_TestCase	1	0	0	0	100%	0.563
🚩	ASM_6014_TestCase	1	0	0	0	100%	0.563
🚩	ASM_6016_TestCase	1	0	0	0	100%	0.532
🚩	ASM_6017_TestCase	1	0	0	0	100%	0.562
🚩	ASM_6018_TestCase	1	0	0	0	100%	0.593
🚩	ASM_6019_TestCase	1	0	0	0	100%	0.531
🚩	ASM_6020_TestCase	1	0	0	0	100%	0.516
🚩	ASM_6021_TestCase	1	0	0	0	100%	0.531
🚩	ASM_6022_TestCase	1	0	0	0	100%	0.516
🚩	ASM_6023_TestCase	1	0	0	0	100%	0.515
🚩	ASM_6024_TestCase	1	0	0	0	100%	0.484
🚩	ASM_6025_TestCase	1	0	0	0	100%	0.5
🚩	ASM_6026_TestCase	1	0	0	0	100%	0.672
🚩	ASM_6027_TestCase	1	0	0	0	100%	0.547
🚩	ASM_6028_TestCase	1	0	0	0	100%	0.141
🚩	ASM_6029_TestCase	1	0	0	0	100%	0.5
🚩	ASM_6030_TestCase	1	0	0	0	100%	0.484
🚩	ASM_6031_TestCase	1	0	0	0	100%	0.531
🚩	ASM_6032_TestCase	1	0	0	0	100%	0.532
🚩	ASM_6033_TestCase	1	0	0	0	100%	0.516
🚩	ASM_6034_TestCase	1	0	0	0	100%	0.578
🚩	ASM_6035_TestCase	1	0	0	0	100%	0.563
🚩	ASM_6036_TestCase	1	0	0	0	100%	0.562
🚩	ASM_6037_TestCase	1	0	0	0	100%	0.609
🚩	ASM_6038_TestCase	1	0	0	0	100%	0.562
🚩	ASM_6039_TestCase	1	0	0	0	100%	0.641
🚩	ASM_6040_TestCase	1	0	0	0	100%	0.579
🚩	ASM_8002_TestCase	1	0	0	0	100%	0.735
🚩	ASM_8003_TestCase	1	0	0	0	100%	0.546
🚩	ASM_8006_TestCase	1	0	0	0	100%	0.516
🚩	ASM_8007_TestCase	1	0	0	0	100%	0.657
🚩	ASM_8008_TestCase	1	0	0	0	100%	0.516
🚩	ASM_8009_TestCase	1	0	0	0	100%	0.516

	ASM_8010_TestCase	1	0	0	0	100%	0.531
	ASM_8011_TestCase	1	0	0	0	100%	0.812
	ASM_8012_TestCase	1	0	0	0	100%	0.562
	ASM_8013_TestCase	1	0	0	0	100%	0.594
	ASM_8014_TestCase	1	0	0	0	100%	0.531
	ASM_8015_TestCase	1	0	0	0	100%	0.687
	ASM_8016_TestCase	1	0	0	0	100%	0.61
	ASM_8017_TestCase	1	0	0	0	100%	0.531
	ASM_8018_TestCase	1	0	0	0	100%	0.563
	ASM_8019_TestCase	1	0	0	0	100%	0.125
	ASM_9001_TestCase	1	0	0	0	100%	0.578
	ASM_9002_TestCase	1	0	0	0	100%	0.531
	ASM_9003_TestCase	1	0	0	0	100%	0.578
	ASM_9004_TestCase	1	0	0	0	100%	0.672
	ASM_9005_TestCase	1	0	0	0	100%	0.531